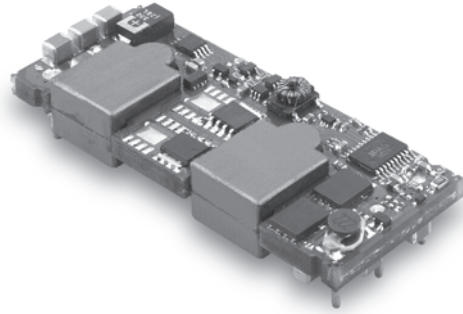


CEB75

S E R I E S

62-84 WATT 1/8 BRICK DC-DC CONVERTERS



Features

- 62-84W Isolated Output
- Eighth Brick Package
- Regulated Outputs
- Efficiency to 92%
- 1500VDC Isolation
- Input Under Voltage Protection
- Over Current Protection
- Over Temperature Protection
- Remote On/Off Control
- Safety Meets UL60950-1

MODEL NUMBER	INPUT VOLTAGE	OUTPUT VOLTAGE	OUTPUT CURRENT		INPUT CURRENT		% EFF.	Capacitor Load max.
			MIN.	MAX.	NO LOAD	FULL LOAD		
CEB75-48S2V5	36-75 VDC	2.5 VDC	0 mA	25 A	60 mA	1463 mA	89	10000µF
CEB75-48S3V3	36-75 VDC	3.3 VDC	0 mA	20 A	60 mA	1528 mA	90	10000µF
CEB75-48S05	36-75 VDC	5.0 VDC	0 mA	15 A	50 mA	1736 mA	90	10000µF
CEB75-48S12	36-75 VDC	12 VDC	0 mA	7 A	70 mA	1902 mA	92	1500µF

NOTE: 1. Nominal Input Voltage 48 VDC

Specifications

INPUT SPECIFICATIONS:

Input Voltage Range.....	48V	36 – 75V
Input Surge Voltage(100ms max.).....	48V	100Vdc max.
Under voltage lockout	power up	35V typ.
	power down	33V typ.
Positive Logic Remote on/off Control (see Note4 & 5)		
Input Filter.....	PI Type	
	12Vout.....	LC Type

OUTPUT SPECIFICATIONS:

Voltage Accuracy.....	±1.0% max.
Transient Response :25% Step Load Change.....	< 400µs
Ripple and Noise, 20MHz BW	100mV pk-pk max.
Temperature Coefficient.....	±0.03%/C max.
Short Circuit Protection.....	Continuous / Hiccup
Line Regulation ¹	±0.2% max.
Load Regulation ²	±0.5% max.
Over Voltage Protection Trip Range, %Vo nom.....	115%-140%
External Trim Adj. Range ⁶	±10%
Current Limit.....	110% - 160% Nominal Output
Start up time	105ms typ., 12Vout:20ms typ.

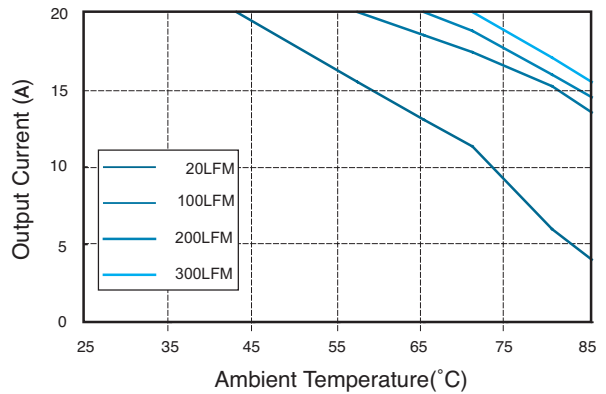
GENERAL SPECIFICATIONS:

Efficiency.....	See Table
Isolation Voltage.....	1500 VDC min.
Isolation Resistance	10 ⁷ Ohms min.
Isolation Capacitance	1000pF typ.
Switching Frequency	250KHz typ
	5Vout.....200 KHz typ.
	12Vout.....350KHz typ.
Over Temperature Protection	120°C typ.
Operating Ambient Temperature Range.....	-40°C to +85°C
Storage Temperature Range	-40°C to +125°C
Dimensions.....	2.30x0.90x0.39 inches
	(58.4x22.9x10.0 mm)
Weight.....	26g

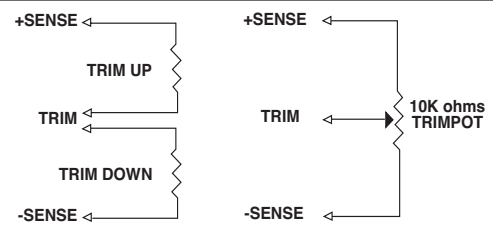
NOTE:

1. Measured From High Line to Low Line.
2. Measured From Full Load to Zero Load.
3. The output ripple and noise is measured with 10µF tantalum and 1µF Ceramic capacitor across output.
4. Logic Compatibility
- Open Collector ref to -Input Module ON
- Open Circuit Module OFF
- < 1.2Vdc
5. Suffix "N" to the Model Number with Negative Logic Remote ON/OFF Module ON
- < 1.2Vdc
- Module OFF
- Open Circuit
6. Trim-up.....Connect a Resistor Between the Trim Pin and+Sense
- Trim-Down.....Connect a Resistor Between the Trim Pin and-Sense

Typical Derating Curve for CEB75-48S3V3



External Output Trim



PIN CONNECTION

Pin	Function
1	+V Input
2	ON/OFF
3	-V Input
4	-V Output
5	-Sense
6	Trim
7	+Sense
8	+V Output

SIZE EB

All Dimensions In Inches(mm)
 Tolerance Inches: x.xx= ±0.02, x.xxx= ±0.010
 Millimeters: x.x= ±0.5, x.xx= ±0.25

